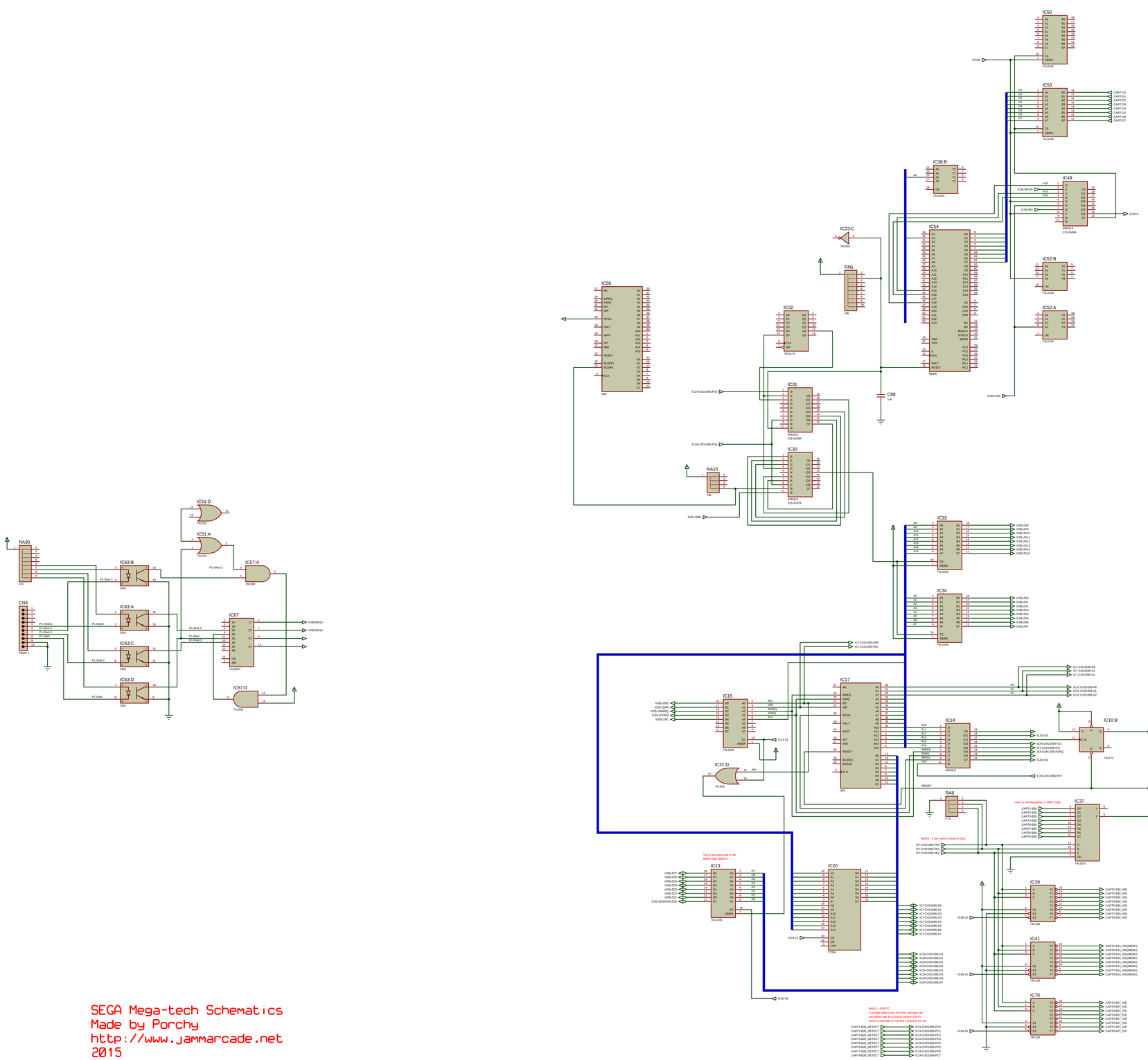
IC50
IC53
IC49
IC38:B
IC23:C
IC54
IC52:B
IC52:A
RN1
IC56
IC32
IC31
IC30
IC33
IC34
IC17
IC15
IC14
IC21:D
IC10:B
RA6
IC37
IC29
IC20
IC13
IC39
IC41
IC70
RA15
C88
IC51:D
IC51:A
IC57:A
IC67
IC57:D
IC63:B
IC63:A
IC63:C
IC63:D
RA30
CN4
IC7-CXD1095-WR
IC7-CXD1095-INT
IC7-CXD1095-RST
IC7-CXD1095-CLR
IC24-CXD1095-P07
IC24-CXD1095-ICS
IC7-CXD1095-ICS
315-5246 (Z80-IORQ
IC20-ICE
IC15-ICE
IC14-M1
IC14-IOR
IC14-IOW
IC14-MREQ
IC14-INT
IC14-RESET
IC14-BUSRQ
IC14-BUSAK
IC14-CLK
IC14-WR
IC14-RD
IC14-A14
IC14-A15
IC14-IORQ
IC14-WAIT
IC14-HALT
IC14-NMI
IC14-RESET
IC14-M1
IC14-IOR
IC14-IOW
IC14-MREQ
IC14-INT
IC14-BUSRQ
IC14-BUSAK
IC14-CLK
IC14-WR
IC14-RD
IC14-A14
IC14-A15
IC14-IORQ
IC7-CXD1095-A0
IC7-CXD1095-A1
IC7-CXD1095-A2
IC24-CXD1095-P04
IC24-CXD1095-P03
IC24-CXD1095-P02
IC24-CXD1095-P01
IC24-CXD1095-P00
IC7-CXD1095-D0
IC7-CXD1095-D1
IC7-CXD1095-D2
IC7-CXD1095-D3
IC7-CXD1095-D4
IC7-CXD1095-D5
IC7-CXD1095-D6
IC7-CXD1095-D7
IC24-CXD1095-D0
IC24-CXD1095-D1
IC24-CXD1095-D2
IC24-CXD1095-D3
IC24-CXD1095-D4
IC24-CXD1095-D5
IC24-CXD1095-D6
IC24-CXD1095-D7
IC49-13
IC49-14
IC49-15
IC45-IC83
IC56-IRFSH
IC56-IM2
IC45-4
IC56-IWR
IC56-IRD
IC56-IORQ
IC56-M1
IC56-INT
IC56-WAIT
IC56-NMI
IC56-RESET
IC56-BUSRQ
IC56-IOR
IC56-IOW
IC56-MREQ
IC56-RFSH
IC56-HALT
IC56-BUSAK
IC56-CLK
IC56-WR
IC56-RD
IC56-A14
IC56-A15
IC56-IORQ
IC56-M1
IC56-INT
IC56-WAIT
IC56-NMI
IC56-RESET
IC56-BUSRQ
IC56-IOR
IC56-IOW
IC56-MREQ
IC56-RFSH
IC56-HALT
IC56-BUSAK
IC56-CLK
IC56-WR
IC56-RD
IC56-A14
IC56-A15
IC56-IORQ
IC56-M1
IC56-INT
IC56-WAIT
IC56-NMI
IC56-RESET
IC56-BUSRQ
IC56-IOR
IC56-IOW
IC56-EM13
IC56-EM16
IC63-ZD0
IC63-ZD1
IC63-ZD2
IC63-ZD3
IC63-ZD4
IC63-ZD5
IC63-ZD6
IC63-ZD7
IC63-ZD0/CK4-ZD0
IC65-D0
IC65-D1
IC65-D2
IC65-D3
IC65-D4
IC65-D5
IC65-D6
IC65-D7
IC65-Z80
IC65-CWR
IC65-CMREQ
IC65-CIORQ
IC65-CRD
IC65-CM1
IC65-CINT
IC65-CWAIT
IC65-CNMI
IC65-CRESET
IC65-CBUSRQ
IC65-CIOR
IC65-CIOW
IC65-CRFSH
IC65-CHALT
IC65-CBUSAK
IC65-CCLK
IC65-CWR
IC65-CRD
IC65-CA14
IC65-CA15
IC65-CIORQ
IC65-CM1
IC65-CINT
IC65-CWAIT
IC65-CNMI
IC65-CRESET
IC65-CBUSRQ
IC65-CIOR
IC65-CIOW
IC65-CMREQ
IC65-CRFSH
IC65-CHALT
IC65-CBUSAK
IC65-CCLK
IC65-CWR
IC65-CRD
IC65-CA14
IC65-CA15
IC65-CIORQ
IC65-CM1
IC65-CINT
IC65-CWAIT
IC65-CNMI
IC65-CRESET
IC65-CBUSRQ
IC65-CIOR
IC65-CIOW
IC65-CRFSH
IC65-CHALT
IC65-CBUSAK
IC65-CCLK
IC65-CWR
IC65-CRD
IC65-CA14
IC65-CA15
IC65-CIORQ
IC65-CM1
IC65-CINT
IC65-CWAIT
IC65-CNMI
IC65-CRESET
IC65-CBUSRQ
IC65-CIOR
IC65-CIOW
IC65-CRFSH
IC65-CHALT
IC65-CBUSAK
IC65-CCLK
CART1-D0
CART1-D1
CART1-D2
CART1-D3
CART1-D4
CART1-D5
CART1-D6
CART1-D7
CART1-B32_IOE
CART1-B32_IOE
CART3-B32_IOE
CART3-B32_IOE
CART5-B32_IOE
CART5-B32_IOE
CART7-B32_IOE
CART7-B32_IOE
CART1-B14_IOE(MEN)
CART1-B14_IOE(MEN)
CART3-B14_IOE(MEN)
CART3-B14_IOE(MEN)
CART5-B14_IOE(MEN)
CART5-B14_IOE(MEN)
CART7-B14_IOE(MEN)
CART7-B14_IOE(MEN)
CART1-B27_ICE
CART1-B27_ICE
CART3-B27_ICE
CART3-B27_ICE
CART5-B27_ICE
CART5-B27_ICE
CART7-B27_ICE
CART7-B27_ICE
CART1-B28_DETECT
CART2-B28_DETECT
CART3-B28_DETECT
CART4-B28_DETECT
CART5-B28_DETECT
CART6-B28_DETECT
CART7-B28_DETECT
CART8-B28_DETECT
IC24-CXD1095-PC0
IC24-CXD1095-PC1
IC24-CXD1095-PC2
IC24-CXD1095-PC3
IC24-CXD1095-PC4
IC24-CXD1095-PC5
IC24-CXD1095-PC6
IC24-CXD1095-PC7
IC7 - CXD1095Q
Ports A and B are set up but not used
$8402 = PORTC - Set as outputs
$8403 = PORTD - Set as outputs
$8404 = PORTE - Set as outputs, used to select cartridge slot
$8406 = REG1 - Used to set input/output of PORTs A-D
$8407 = REG2 - Used to set input/output of PORT E
$8C00 - Any read or write to this address will
clear the RESET line to the CXD1095Q's.
The code reads this address once all the registers are setup.
There are several writes to this address but am unsure what they do.
PAL16L8 @ IC14
12 - /CE at address 0x0 - $2FFF
/CE at address 0x7000 - $7FFF
13 - RAM at $2000 - $3FFF
15 - /CS at address $6400
14 - /CS at address $6000
17 - /CLK at address $6200
19 - RAM at address $4000 - $5FFF
IC24 - CXD1095Q
Ports A and B are set up as inputs
$8402 = PORTC - Set as inputs - "Cartridge present" signals
$8403 = PORTD - Set as outputs
$8404 = PORTE - Set to in or input, bits 1-3 are outputs
$8406 = REG1 - Used to set input/output of PORTs A-D
$8407 = REG2 - Used to set input/output of PORT E
This is the data path to the
68000 data ($8000)
Used to set MegaDrive or SMS mode
$8404 - 3 bits used to select 8 slots
$8402 = PORTC
Cartridge detect pins from the cartridge slot
are pulled high by a pullup resistor (5vRT).
When a cartridge is inserted it grounds the pin.
SEGA Mega-tech Schematics
Made by Porchy
http://www.jammarcade.net
2015